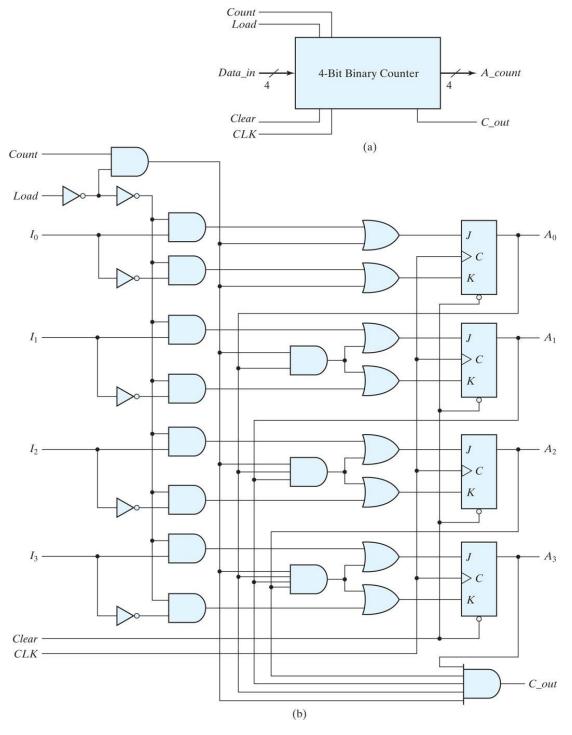
## 4-bit synchronous binary counter w/ parallel load (Figure 6.14 of text)



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