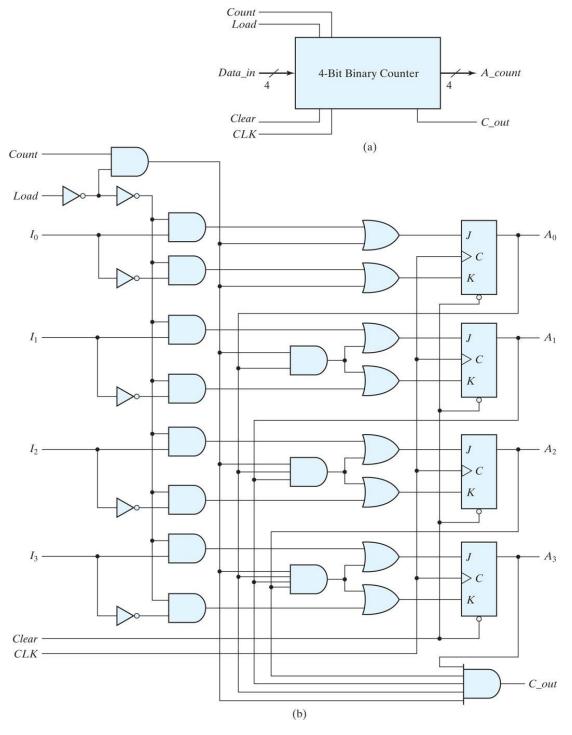# 4-bit synchronous binary counter w/ parallel load (Figure 6.14 of text)