

Learn-/Training Document

Siemens Automation Cooperates with Education (SCE) | From Version V14 SP1

TIA Portal Modul 012-110 Specified Hardware Configuration with SIMATIC S7-1500 CPU 1512SP F-1 PN

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SIMATIC Controllers

- SIMATIC CPU 1512SP F-1 PN with Software Order no.: 6ES7512-1SK00-4AB2
- SIMATIC ET 200SP Analog Modules Expansion Order no.: 6ES7155-6AU00-0AB6

SIMATIC STEP 7 Software for Training

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Specific hardware configuration – SIMATIC S7-1500 CPU 1512SP F-1 PN

1 Objective

In this section, you will start by learning how to **create a project**. In a portion of a task, you will also be shown how to recognize previously installed **hardware** and add it to a project using the *TIA Portal*. You will then be shown how to **configure the hardware**.

The SIMATIC S7 controllers listed in chapter 3 can be used.

2 Requirement

Nothing is required from other chapters to successfully complete this chapter.

3 Hardware and software required

- 1 Engineering Station: hardware and operating system (for additional information, see Readme file on the TIA Portal Installation DVDs)
- 2 SIMATIC STEP 7 Professional software in the TIA Portal V14 or higher
- 3 SIMATIC S7-1500 controller with ET 200SP CPU Firmware V2.0 or higher with memory card and at least 16 DI/16 DO as well as 2 AI/1 AO

Example configuration 1x controller CPU 1512SP F-1 PN with bus adapter BA 2xRJ45 2x IO module 8x digital input DI 8x24VDC HF 2x IO module 8x digital output DQ 8x24VDC/0.5A HF 2x IO module 2x analog input AI 2xU/I 2.4-wire HS 1x IO module 2x analog output AQ 2xU/I HS 1x server module

4 Ethernet connection between the engineering station and controller





2 STEP 7 Professional (TIA Portal) V14 or higher

3 SIMATIC S7-1500 controller with ET 200SP CPU

4 Theory

4.1 Automation system SIMATIC S7-1500 with ET 200SP CPU

The SIMATIC S7-1500 automation system with the ET 200SP CPU is a modular control system for the medium-level and high-level performance range. A comprehensive range of modules is available for optimum adaptation to the automation task.

The CPUs of the ET 200SP CPU series represent a further development of the ET 200S CPU series and have the following new performance features:

- Increased system performance
- Integrated Motion Control functionality
- PROFINET IO IRT
- OPC UA
- STEP 7 language innovations while retaining proven functions

4.2 Installation and operation of the SIMATIC ET 200SP CPU

SIMATIC ET 200SP is mounted on a standard mounting rail (7) and consists of the CPU/interface module (1) with bus adapter (6), up to 64 input and output modules for digital and analog signals (4) plugged onto BaseUnits (2), (3) and a terminating server module (5). Communications processors and function modules for special tasks, such as PROFIBUS communication, IO-Link, PROFlenergy and stepper motor control, are also used when necessary.



As a programmable logic controller (PLC), SIMATIC ET 200SP CPU monitors and controls a machine or process with the S7 program. The IO modules are queried and addressed in the S7 program using input addresses (%I) and output addresses (%Q).

The system is programmed with the STEP 7 Professional software in the TIA Portal.

4.2.1 Range of modules

The SIMATIC S7-1500 controller of the ET 200SP CPU series is a modular automation system with the following range of modules:

Central processing units (CPUs) with pluggable bus adapters

The CPUs, which are available for different performance levels, run the user program. In addition, the integrated system power supply provides power to the other modules over the backplane bus.

Bus adapters enable users to use the connection system of their choice.

Additional properties and functions of the CPU:

- Communication via Ethernet
- Communication via PROFIBUS/PROFINET
- HMI communication on operator control and monitoring devices
- Web server
- Integrated technology functions (e.g.: PID controller, Motion Control, etc.)
- System diagnostics
- Trace functionality
- Integrated security (e.g.: know-how, copy, access, data integrity protection)



BaseUnits

As universal base modules for electrical and mechanical connection of IO-modules.

They are available in a light variant BU..D, which opens a new potential group over the backplane bus for the power supply, and a dark variant BU..B, which continues the potential group. Thus, at least one BaseUnit BU..D must be used in order to supply at least one potential group with voltage. The IO-modules are plugged onto the BaseUnits.



IO modules

For digital input (DI) / digital output (DQ) / analog input (AI) / analog output (AQ). They are available in variants for 24 V DC ①, 400 V AC ② and F-modules (fail-safe) ③.



Communication modules (CM)

For a point-to-point connection (PtP) ① or for connection to the communication systems IO-Link ②, AS-i ③, etc.



Technology modules (TM)

Modules for detecting high-frequency pulses, for weighing and for positioning, etc.



Server module

As termination of the ET 200SP system installation.

It can be used as a holder for 3 standby fuses. It serves as a terminating resistor for the backplane bus and is thus required.



SIMATIC memory card

Up to a maximum of 32 GB for storing program data and for easier replacement of CPUs during maintenance.



4.2.2 Example configuration

The following configuration of a SIMATIC S7-1500 controller of the ET 200SP CPU series is used for the program example in this document.



- ① Central processing unit CPU 1512SP F-1 PN with bus adapter BA 2xRJ45
- 2 2x IO module 8x digital input DI 8x24VDC HF (2x)
- ③ 2x IO module 8x digital output DQ 8x24VDC/0.5A HF (2x)
- (4) 2x IO module 2x analog input AI 2xU/I 2.4-wire HS (2x)
- (5) IO module 2x analog output AQ 2xU/I HS (1x)
- 6 Server module

4.3 Operator controls and display elements of the CPU 1512SP F-1 PN

The following figure shows the operator controls and display elements of a CPU 1512SP F-1 PN and a BA 2xRJ45 bus adapter. For other CPUs, the number of elements and their arrangement differ from this figure.

4.3.1 Front view of the CPU 1512SP F-1 PN with BA 2xR bus adapter



- ① Mounting rail release
- 2 Labeling strips
- ③ LEDs for status and error displays
- (4) LED for display of supply voltage
- (5) Mode switch
- 6 Slot for the SIMATIC memory card
- ⑦ Connection for supply voltage (included in the product package)
- (8) Cable support and fixing means for Port P3 of the PROFINET interface
- (9) LEDs for status displays of the PROFINET interface for ports P1, P2 and P3
- 1 Port P3 of the PROFINET interface: RJ45 socket on the CPU
- $(\underline{1})$ Single view of the bus adapter
- Derived Port P1 R of the PROFINET interface: RJ45 socket on bus adapter BA 2xRJ45
- (B) Port P2 R of the PROFINET interface: RJ45 socket on bus adapter BA 2×RJ45

4.3.2 Status and error displays

The CPU 1512SP-1 PN and bus adapter BA 2xRJ45 are equipped with the following LEDs:



- ① RUN/STOP-LED (yellow/green LED)
- 2 ERROR LED (red LED)
- ③ MAINT LED (yellow LED)
- ④ LINK RX/TX-LED for ports X1 P1 and X1 P2 (green LEDs on bus adapter)
- (5) POWER LED (green LED)
- 6 LINK RX/TX LED for port X1 P3 (green LED on CPU)

4.3.3 SIMATIC memory card

A SIMATIC micro memory card (MMC) is used as a memory module for the CPUs. This is a preformatted memory card that is compatible with the Windows file system. It is available in different memory sizes and for the following purposes:

- Portable data storage medium
- Program card
- Firmware update card

The MMC **must** be inserted for operation of the CPU because the CPUs have no integrated load memory. A commercially available SD card reader is needed for reading/writing of the SIMATIC memory card with the programming device or PC. Files can then be copied directly to the SIMATIC memory card with Windows Explorer.

Note:

 It is recommended that the SIMATIC memory card only be inserted and removed when the CPU is in POWER OFF state.

4.3.4 Mode switch

You can use the mode switch to set the current operating mode of the CPU. The mode switch is designed to toggle 3 switch positions.

Position	Meaning	Explanation
RUN	RUN mode	The CPU runs the user program.
STOP	STOP mode	The CPU does not run the user program.
MRES	Memory reset	Position for memory reset of the CPU

You can also switch the operating mode (**STOP** or **RUN**) with the button under Online & diagnostics on the CPU operator panel of the STEP 7 Professional V14 software.

In addition, the operator panel contains an **MRES** button for resetting the memory and shows the status LEDs of the CPU.

PLC_1 [CPU 15	2SP F-1 PN]
RUN / STOP	RUN
ERROR	STOP
MAINT	MRES

4.3.5 Memory areas of the CPU 1512SP F-1 PN and the SIMATIC memory card

The following figure shows the memory areas of the CPU and the load memory on the

SIMATIC memory card.

In addition to the load memory, it is possible to load additional data onto the SIMATIC memory card with Windows Explorer. Recipes, data logs, project backups and other documentation for the program can be loaded there, for example.



Load memory

Load memory is non-volatile memory for code blocks, data blocks, technology objects and the hardware configuration. When these objects are downloaded to the CPU, they are first stored in the load memory. This memory is located on the SIMATIC memory card.

Work memory

Work memory is volatile memory that contains the code and data blocks. It is integrated in the CPU and cannot be expanded. The work memory is divided into two areas in S7-1500 CPUs:

 \rightarrow Code work memory:

Code work memory contains parts of the program code relevant for execution.

 \rightarrow Data work memory

Data work memory contains parts of the data blocks and technology objects relevant for execution.

During the operating mode transition from POWER ON to Start and from STOP to Start, tags of global data blocks, instance data blocks and technology objects are initialized with their start values. Retentive tags keep their current values saved in the retentive memory.

Retentive memory

Retentive memory is non-volatile memory for backing up certain data in case of power failure. The tags and operand areas defined as retentive are saved in retentive memory. This data is retained for the duration of a shutdown or power failure.

All other program tags are set to their start values with an operating mode transition from POWER ON to Start and from STOP to Start.

The content of the retentive memory is deleted by the following actions:

- Memory reset
- Reset to factory settings

Note:

 Certain tags of technology objects are also saved in retentive memory. These are not deleted with a memory reset.

4.4 Programming software STEP 7 Professional V14 (TIA Portal V14)

The STEP 7 Professional V14 (TIA Portal V14) software is the programming tool for the following automation systems:

- SIMATIC S7-1500
- SIMATIC S7-1200
- SIMATIC S7-300
- SIMATIC S7-400
- SIMATIC WinAC

With STEP 7 Professional V14, the following system automation functions can be used:

- Configuration and parameter assignment of the hardware
- Specifying the communication
- Programming
- Testing, commissioning and service with operational/diagnostic functions
- Documentation
- Creation of visualizations for SIMATIC Basic Panels with integrated WinCC Basic.
- Visualization solutions for PCs and other panels can also be created with other WinCC packages.

All functions are supported by an extensive online help.

4.4.1 Project

You create a project in the TIA Portal for performing an automation and visualization task. A project in the TIA Portal contains the configuration data for the installation and networking of devices as well as the programs and the configuration of the visualization.

4.4.2 Hardware configuration

The *hardware configuration* contains the configuration of the devices, consisting of the automation system hardware, the intelligent field devices and the hardware for visualization. The configuration of the networks specifies the communication between the various hardware components. Individual hardware components are inserted from catalogs into the *hardware configuration*.

The automation system hardware is composed of controllers (CPUs), signal modules for input and output signals (SMs) and communication and interface modules (CP, IM). System power supply and load power supply modules (PS, PM) are also available for supplying energy to the modules.

The signal modules and the intelligent field devices connect the input and output data process that is to be automated and visualized to the automation system.





The hardware configuration enables automation and visualization solutions to be downloaded to the automation system and the controller to have access to the connected signal modules.

4.4.3 Centralized and distributed automation structure

Figure 1 shows an automation structure containing both centralized and distributed structures.

In centralized structures, the input and output signals of the process are transmitted over conventional wiring to the signal modules, which are connected directly to the controller. Conventional wiring means that sensors and actuators are connected using 2-wire or 4-wire cables.

These days, the use of distributed structures is predominant. In this case, the sensors and actuators only have to be conventionally wired up to the signal modules of the field devices. The signal is transmitted from the field devices to the controller via an industrial communication system.

Both classic fieldbuses such as PROFIBUS, Modbus and Foundation Fieldbus and Ethernet-based communication systems such as PROFINET are used for industrial communication.

The communication system can also be used to connect intelligent field devices on which independent programs run. These programs can also be created with the TIA Portal.

4.4.4 Planning the hardware

Before you can configure the hardware, you must plan it. In general, you start by selecting the required controllers and the number needed. Next, you select the communication modules and signal modules. The signal modules are selected based on the number and type of inputs and outputs needed. Finally, an adequate power supply must be selected for each controller or field device.

The functionality needed and the environmental conditions are of critical importance for planning the hardware configuration. For example, the temperature range in the application area is one of the limiting factors for selecting possible devices. Fail-safe capability could be another requirement, for example.

The <u>TIA Selection Tool</u> (select Automation technology \rightarrow TIA Selection Tool and follow the instructions) offers you support.

Notes

- TIA Selection Tool requires Java.
- To obtain the device specifications online, look for the manual described as "Product Manual" or "Manual" among the various manuals listed.

4.4.5 TIA Portal – Project view and portal view

The TIA Portal has two important views. When started, the TIA Portal displays the portal view by default. This view makes the first steps easier, especially for beginners.

The portal view provides you with a task-oriented view of the tools for working with the project. Here, you can quickly decide what you want to do and open the tool for the task in hand. When needed, the system switches to the project view automatically for a selected task.

Figure 2 shows the portal view. You have the possibility to change between this view and the project view at the bottom left.



Figure 2: Portal view

The project view, as shown in Figure 3, is used for the hardware configuration, programming, creation of the visualization and many other tasks.

With the default setting, the menu bar containing the toolbars is at the top, the project tree containing all the components of a project is on the left and the so-called task cards containing instructions, libraries, etc. are on the right.

If an element is selected in the project tree (for example, the device configuration), it is displayed in the middle and can be worked on there.

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Figure 3: Project view

4.4.6 Basic settings for the TIA Portal

- → The user can make customized default settings for certain settings in the TIA Portal. A couple of important settings are shown here.
- \rightarrow In the menu of the Project view, select \rightarrow "Options" and then \rightarrow "Settings".

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- → A basic setting is the choice of user interface language and the language for the program display. English is used for both settings in this document.
- → In the → "General" item of "Settings", select → "English" for "User interface language" and → "International" for "Mnemonics".

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Note:

- These settings can always be changed back and forth between "English" and "International".
- → To use safety CPUs (e.g. CPU 1512SP F-1 PN) without the use of safety technology, we recommend you disable automatic generation of the safety program before creating a project.
- → In the → "STEP 7 Safety" item of "Settings", clear the → "Generate default fail-safe program" check box.



4.4.7 Setting the IP address on the programming device

In order to program the CPU of a SIMATIC S7-1500 controller from a PC, programming device or laptop, a TCP/IP connection is needed.

For the computer and SIMATIC S7-1500 to communicate with one another via TCP/IP, it is important that the IP addresses of both devices match.

First, it is shown how to set the IP address of a computer with Windows 10.

 \rightarrow Select the network icon in the taskbar at the bottom and click on \rightarrow "Network settings".



→ In the Network settings window that opens, click → "Ethernet" and then → "Change adapter options".



→ Select the desired → "LAN connection" you want to use to connect to the controller and click → "Properties".



 \rightarrow Select the \rightarrow "Properties" for \rightarrow "Internet Protocol Version 4 (TCP/IPv4)".

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→ You can now use the following IP address, for example. → IP address: 192.168.0.99 and enter the following → subnet mask 255.255.255.0. Afterwards, apply the settings. (→ "OK")

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Default gateway:		+		•]	
Obtain DNS server address	automatica	iy.							
Use the following DNS server	er addresse	ST .							
Preferred DNS server:		÷		i.]	
Alternate DNS server:		¥		÷		4]	
Validate settings upon exit					1	4	Adva	nces	d

4.4.8 Setting the IP address in the CPU

The IP address of a SIMATIC S7-1500 is set as follows.

 \rightarrow Double-click the icon for the Totally Integrated Automation Portal. (\rightarrow TIA Portal V14)



 $\rightarrow~$ Select \rightarrow "Online & Diagnostics" and open the \rightarrow "Project view".



→ In the project tree, select the previously set network adapter under → "Online access". When you click → "Update accessible devices", you see the IP address (if already set) or the MAC address (if the IP address has not yet been assigned) of the connected SIMATIC S7-1500. Select → "Online & diagnostics".



→ Under → "Functions", you will find the → "Assign IP address" item. Enter the following IP address and subnet mask, for example: → IP address: 192.168.0.1 → Subnet mask: 255.255.255.0. Click → "Assign IP address", and this new address will be assigned to your SIMATIC S7-1500.



Note:

- The IP address of the SIMATIC S7-1500 can also be set via the display on the CPU, if this is enabled in the hardware configuration.
- → If the assignment of the IP address was not successful, you receive a message in the \rightarrow "Info" window \rightarrow "General" tab.

				S Properties	33	Info	🗓 Diagnost	ics 📄	
General	Cross-references	Compile	Energy Suite						
010	Show all messages								
1 Message					Go to	120	Date	Time	
🕄 💌 The IP	address could not be assi	gned.				?	11/23/2017	1:33:48 PM	~
😧 Th	e set command could not l	be executed.					11/23/2017	1:33:48 PM	>

4.4.9 Formatting a memory card in the CPU

- → If the IP address could not be assigned, the program data on the CPU must be deleted. This is done in 2 steps: → "Format memory card" and → "Reset to factory settings".
- \rightarrow First, select the \rightarrow "Format memory card" function and click the \rightarrow "Format" button.



 \rightarrow Confirm the prompt "Do you really want to format the memory card?" with \rightarrow "Yes".

Online &	diagnostics (0241:000024)	×
4	Do you really want to format the memory card?	
	Yes	-

 \rightarrow Stop the CPU if necessary. (\rightarrow "Yes")

Online &	diagnostics (0241:0	100025) ×
4	Formatting a mem you want to stop t	ory card is only permitted in STOP mode. Do he CPU?
		Yes No

4.4.10 Resetting the CPU to factory settings

→ Before you can reset the CPU memory, you must wait until the formatting of the CPU is finished. Then you need to select → "Update accessible devices" again and select → "Online & diagnostics" of your CPU. To reset the controller, select the → "Reset to factory settings" function and click → "Reset".

		Online access + - InnatOD Enter				
Devices		5				
		Origination Function Assorger Praditions Sections Promotion Information Assorger Proceedings Assorger Proceedings Assorger Proceedings Assorger Proceedings Sections Sections	Reset to factory settings If address recorded device name:	192 108 0 2 (95,1 (e) Networt Paddress (c) Deleter Paddress (c) Networt	•	
Card Reader/USB memory	nt Ma			S Properties	Diagramitica	Clif
Second Contraction		General Guns reference	s Compile Energy Suite			
		A O Show of messages Annuale Scanning for devices complet	····	Gana 1 01219441 Pours.	Dete Torie 110230817 1.3715	L PM

 \rightarrow Confirm the prompt "Do you really want to reset the module?" with \rightarrow "Yes".



 $\rightarrow~$ Stop the CPU if necessary. ($\rightarrow~$ "Yes")



4.4.11 Reading out the firmware version of the CPU 1512SP F-1 PN

→ Before you can read out the firmware version of the CPU 1512SP F-1 PN, you must select →
 "Update accessible devices" again and select → "Online & diagnostics" of your CPU 1512SP
 F-1 PN. With the menu command → "Diagnostics" → "General" you can read out the short description, order number, hardware version and firmware version.

		Online access + - InnatOD Tree			
Devices					
9	발	· Ciagnostics	General		1
Confere access ComparyInde interfaces ComparyInde Compar	(1) (1)	Degrocita zatua Degrocita tuño Cytó inte Vernary Hernary Hostifel Tisteface[21] Hastifel	Module (Institute growthe Statistics) Statistics (Institute growthe Statistics) Statistics (Institute growthe Statistics) Statistics (Institute growthe Statistics) Statistics (Institute growthe Statistics) Statistics (Institute growthe Statistics) Statistics (Institute growthe Statistics) Statistics (Institute growthe Statistics) Statistics	000 1512577-1 M 4057 513 15001 0480 8 V2.1.0 - 0 T	
Calification (M-F) Direct Virtual Adapter Control (Loce) Control (Loce) Control (Loce) Control (Loce) Control (Loce) Control (Loce)			Module information	Chilamnan	
Card Reader/USB memory	-			Stroperties State Subaprestics	CAL
		General Guess reference	es Compile Energy Suite		
		Lincarge Scarourge for devices come	dated to investiga intel ¹	Go to 1 Date Time	-

5 Task

Create a project and configure the following modules of your hardware that correspond to the trainer package **SIMATIC CPU 1512SP F-1 PN with software**.

- 1x CPU 1512SP F-1 PN for ET 200SP, central processing unit with 300 KB work memory for program and 1 MB work memory for data, 1 interface, PROFINET IRT with 3-port switch, 48 ns bit performance, memory card (order number: 6ES7512-1SK01-0AB0)
- 2x DI 8x24VDC/0.5A HF (order number: 6ES7131-6BF00-0CA0)
- 2x DQ 8x24VDC/0.5A HF (order number: 6ES7132-6BF00-0CA0)
- 1x server module (order number: 6ES7 193-6PA00-0AA0)

6 Planning

Because a new system is involved, a new project must be created.

The hardware is already specified for this project with the SIMATIC CPU 1512SP F-1 PN with software trainer package. For this reason, no selection has to be made. Instead, the listed modules of the trainer package only have to be inserted in the project. In ensure the correct modules are inserted, the order numbers from the task should be checked one more time directly on the mounted devices.

It is customary to start with the CPU and then insert the signal modules in the associated slots. See Table 1.

For the configuration, the Ethernet interface must be set for the CPU and the address ranges of the digital inputs and outputs must be adapted.

Module	Order number	Slot	Address range
CPU 1512SP F-1 PN	6ES7512-1SK01-0AB0	1	
DI 8x24VDC/0.5A HF	6ES7131-6BF00-0CA0	2	DI 0
DI 8x24VDC/0.5A HF	6ES7131-6BF00-0CA0	3	DI 1
DQ 8x24VDC/0.5A HF	6ES7132-6BF00-0CA0	4	DQ 0
DQ 8x24VDC/0.5A HF	6ES7132-6BF00-0CA0	5	DQ 1
Server module	6ES7 193-6PA00-0AA0	6	

Table 1: Overview of planned configuration

To complete the configuration, the HW configuration must be compiled and downloaded. Configuration errors can be detected during compilation and incorrect modules can be detected when the controller is started (only possible when configured hardware is present and identical). The checked project must be saved.

7 Structured step-by-step instructions

You can find instructions on how to perform planning below. If you already have a good understanding of everything, it is sufficient to focus on the numbered steps. Otherwise, simply follow the steps of the instructions illustrated below.

7.1 Creating a new project

 \rightarrow Double-click the icon for the Totally Integrated Automation Portal. (\rightarrow TIA Portal V14)



 \rightarrow In the portal view, select \rightarrow "Create new project" under the "Start" menu command.

Ma Siemens		
Start		
Devices in sectors in	3 ⁵	 Open existing project Create new project Migrate project
Marine A Sectorships	-	
Vitestice.	- Ta 1	Welcome Tour This supp

 \rightarrow Edit Project name, Path, Author and Comment as appropriate and click \rightarrow "Set".

Project name:	012_110_CPU15125P F-1 PN	
Pathi	C100_TA_Portel	
Version:	V14 SP1	
Author:	mde	
Comment:	1	0

 \rightarrow The project is created and opened, and the "Start" menu "First steps" opens automatically.

7.2 Inserting the CPU 1512SP F-1 PN

→ In the → "Start" → portal, "First steps" → select "Devices & networks" → "Configure a device".

a k		First steps				
Devices &	Open existing project	Project: *012_1	10_0/015125P F-1	PN" was a	pened successfully. Please select the r	vext step:
notaerita 📬 PLC proposessing	 Crinite now project Migrate project 	-	1			
Motion A Inchesiogy Drive parameterization	Welcome Tout	S -1		66	Configure a device	
Visiasization 💋	🥥 First steps			* *	Write PLC program	
Disgnoettes	 Installed software Help 	-i	finis ministration	£ 49	Parameterias delve	
	🚯 User Interface language			P	Configure on HM screen	

- \rightarrow In the "Devices & networks" portal, the "Show all devices" menu opens.
- $\rightarrow~$ Switch to the "Add new device" menu.

n leg		Add new device		
Devices &	Show all devices	Device fame.		
nc programming 🧇	All new device	· Caracters • Caracters	Drine	
Martian A. Inchantingy		Centerfairte		
Delve personalasitikat 🏠	Configure networks	• Stiller State	Par and a second se	
Visialization 💋			Version	101
Ordina & Diagnostica			17222	
	· Help	PExponen		
		Dires		
		1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.		

→ The specified model of the CPU will now be added as a new device (Controllers → SIMATIC ET 200 CPU → ET 200SP CPU → CPU 1512SP F-1 PN → 6ES7512-1SK01-0AB0 → V2.1)

			Totally Integrated Automation PORTAL
Devices & S	 Show all devices. 	Add new davice	
PLC programmeling 🦃	Add new device	Controlete Controlete Controlete Controlete Subtraction 32-1202 Subtraction 12-1202 Subtraction 1	Dear Dear
Drive personalization	🕜 Cantigura networks		CRU16125474794 Antole No. 0517512-5901-6460 Velson: v2.1 •
Ordine & P		Experime # #57 512 straboles Fx permine # #57 512 straboles Fx permine # # #12005 CPU # # #12005 CPU # # #12005 CPU # # #12005 CPU # # #12005 CPU	Desception: Publicate DPU with north memory 200 XD code and 1. VB date, can be used for setting applications: support consistent setting support Pointale V2, date to its metacologies in the setting appropriate service, simplicate methoday bucklence: which used and code?
	174	2mm	Productor TO compare in the provide in Term participation compared in TOTAL TO A Down in conserve AMP, AMPD, associated protocol SCHM, Incruire Departicipation (ST - temportation), Web servers, DHS (Serve, DPC (a) mover of Bale anternation and the server. TOHS (Serve, DPC (a) mover of Bale anternation), and the server, ST - mover of Bale anternation and the server. Sch (Server), Server mathing, Ramitme applicate, Streventer SC, 1

 \rightarrow Assign a device name (Device name \rightarrow "CPU_1512SP").

Device name:		
CPU1512_SP		

 \rightarrow Select "Open device view".

🔽 Open device view

 \rightarrow Click "Add".



Notes:

- A desired CPU may come in multiple variants that differ in their functional scope (work memory, integrated memory, technology functions, etc.). In this case, you should ensure that the selected CPU corresponds to the actual hardware present.
- Different firmware versions are often available for the hardware. In this case, it is recommended that the (already preselected) latest firmware be used and that the CPU be upgraded if necessary.

→ The TIA Portal now automatically changes to the project view and displays the selected CPU 1512SP F-1 PN in slot 1 in the device configuration.



Note:

 You can now configure the CPU there according to your specifications. Settings for the PROFINET interface, the startup characteristics, the cycle, the communication load and many other options are possible here.

7.3 Configuration of the Ethernet interface of the CPU 1512SP F-1 PN

- → Double-click the CPU to select it.
- → In the → "Properties" tab, open the → "PROFINET interface [X1]" menu and select the → "Ethernet addresses" entry there.

	012_110_CPU15125PT-1 PN + CPU1512_5P(CPU 15125PT-1 PN)	_ # # X Manhaure saturas # 1
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- → Under "Interface networked with", there is only one entry available: "Not networked".
- \rightarrow Add an Ethernet subnet with the \rightarrow "Add new subnet" button.

Ethemet addresses		
Interface networked with		
Subnet:	Not networked	-
	Add new subnet	

 \rightarrow Keep the preset "IP address" and "Subnet mask" here.

- CPU1512_SP (CPU 1512	SPF-1 🖬 🖭 🍊 🔛	Topolo	gy view 🚺 Netv	work view 🏼 🔐 Devic	e view
CPU1512_SP [CPU 15125	29 F-1 PN]	S Prop	erties Info	Diagnostics	1
General 10 tags	System constants	Texts			
General Failsafe PROFINET interface [X1] General	Ethernet addresse Interface netw	rs			
Ethemet address et Time synchronization	•	Subnet:	PNRE_1 Add new subme	t	•
Operating mode Advanced options Web server access Hardware identifier Startup Cycle Communication load System and clock me SIMATIC Memory Card System diagnostics	H IP protocol	1	Set IP address in th IP address: Subnet mask: Use router Touter address IP address is set di	e project 192 - 168 - 0 1 255 - 255 - 255 - 0 8 - 0 - 0 rectly at the device	
PLC elemis Web server General Automatic update User management Security Watch tables Userdefined pages	PROFINET	Tdevice name: nverted name	PROFINET device na Generate PROFINET cpu1512_sp cpu1512xbsp68ae	ime is set directly at the di device name automatical	evice ly

7.4 Configuration of fail-safe operation of the CPU 1512SP F-1 PN

 \rightarrow Change to the \rightarrow "F-activation" item of the \rightarrow "Fail-safe" menu and select \rightarrow "Disable F-activation" there.



 \rightarrow Answer the question "Do you want to continue?" with \rightarrow "Yes".

Turning	g off the F-activation (2501:000020)	×
4	Turning off the F-activation CPU1512_SP will deactivate all fail-safe blocks. Do you want to continue?	1
	Ves	

7.5 Configuration of access level for the CPU 1512SP F-1 PN

→ Change to the → "Protection & Security" menu and select → "Full access incl. fail-safe (no protection)" for the access level.

CTU1512_51	CPU 15126	1010	NI			1	Ropert	ies 🏌	info 🕕	& Diagnostics	T. Anto
General	IO tags	Syste	em constants	Texts							
General Fail-safe Fail-safe PROFINET in Startup Cycle Communice Sustem and	tenface (X1) tion load	-	Protection & Sec Access level Select the acce	curity	nc						
SIMATIC Mer System diag	nory Cerd prostics			Access	evel	PRAL	A Read	ccess Write	Fail-safe	Access permission Password	
PLC alarms			Full a	access incl. fail	safe (no protection)	~	~	~	~		~
 Web server DNS configu Multilingual Time of day 	ration support		O Full e O Read O HM e	access (no pro l'access access access (comple	tection)	***	**	*			¢ [11]
 Protection & OPC UA Configuratio Connection Overview of 	Security in control resources addresses	-	Full access incl. TA Portal users No password is	fail-safe (no p and HMI applic required.	rotection): ations will have acce	ss to all st	n andard and	teil-cate to	nctions.		>]]]]

Note:

 The "Full access incl. fail-safe (no protection)" setting is recommended because a safety program is not created here and we also do not have to assign a password.

7.6 Inserting the digital input modules DI 8x24VDC HF

Select the correct module in the hardware catalog (\rightarrow Hardware catalog \rightarrow Catalog \rightarrow DI \rightarrow DI 8x24VDC HF (order number 6ES7131-6BF00-0CA0) \rightarrow Version: V2.0)





 \rightarrow Insert the two digital input modules by dragging them into slots 2 and 3.

Note:

For selection of the digital input module, you can simply enter the order number in the Search field and click the "Search down" is icon. The hardware catalog is opened at the right place.

Hardware catal	eg)		50	
Options				83
				Han
✓ Catalog				dwa
6E57 131-68F00-0	CAD	164	NT	e c
Filter Profil	el 🕳 Search down		-	12.1
ET 2005F CP Interface mo BusAdopter DI	U Starts the search in the selection in the edit Hardware catalo	the hardware or and search	catal es do	og be wn.
DI 8x24V0 DI 8x24V0 DI 8x24V0 DI 8x24V0 DI 8x24V0 DI 8x24V0 DI 9x120.	DC ST DC BA DC SRC BA 230VAC ST		u	tine tools
 Di 16x24V Di 8x24V0 6657 / Di 8x24V0 	VDC ST DC HF 131-68F00-0CAD			(Task
DI 8/04/4/	UR HF VDC HF		*	E

Note:

 When you double-click a module in the Hardware catalog, it is inserted at the next available suitable slot.

7.7 Inserting the digital output modules DQ 8xDC24V / 0.5A HF

- → Select the correct module from the hardware catalog (→Hardware catalog → Catalog → DQ → DQ 8xDC24V/0.5A HF (order number 6ES7132-6BF00-0CA0) → Version: V2.0)
- \rightarrow Insert the digital output modules in slots 4 and 5.



Note:

- If you have skipped a slot, you must close the gap before compiling to avoid an error message.

7.8 Changing components in the hardware configuration

→ If it is necessary to change a component in the hardware configuration with a newer version or another type, this can be done by right-clicking the component and selecting "Change device".
 A dialog opens where you can select a new component for the exchange and confirm the selection with "OK". (→ Change device → OK)



Note:

 If the new component is not available for selection, it is not compatible with the previous component. In this case, the old component must be deleted first and the new component must then be inserted from the hardware catalog.

7.9 Inserting the server module

→ Select the correct server module with the correct order number and version from the hardware catalog. Insert the server module in slot 5. (→ Hardware catalog → Server module → 6ES7 193-6PA00-0AA0 → Version: V1.1)



Note:

 If you forget to insert the server module, it will be created automatically when the device configuration is compiled.

7.10 Configuration of address ranges for DI/DO: 0...1

→ The address ranges of the input and output cards must now be checked and, if necessary, adapted. Inputs and outputs (DI/DO) should have an address range of 0...1. (→ Device overview → DI → I addresses: 0/1 → D0 → Q addresses: 0/1)



Note:

 To display and hide the device overview, click the small arrows next to "Device data" on the right side of the hardware configuration.

Device data			
evice data	18	12	
ice dau	4	2	
t × 1	-	Tier C	
5		110	
	11	3	

7.11 Configuration of the potential groups of the BaseUnits

→ To change the potential group of a BaseUnit, select the associated module and open the "Potential group" section in the general properties. (Slot 3 → Properties → General → Potential group)



 \rightarrow Select the "Enable new potential group (light BaseUnit)" option.

otential group	
	O Use potential group of the left module (dar
	Enable new potential group (light BaseUnit)



 \rightarrow The BaseUnit in the configuration has now become a light BaseUnit.

→ Repeat these steps for slots 4 and 5 and compare the device configuration with the figure below.



7.12 Saving and compiling the hardware configuration

→ Before you compile the configuration, you should save your project by clicking the → Save project button. To compile your CPU with the device configuration, first select the → "CPU_1512SP [CPU 1512SP F-1 PN]" folder and click the → "Compile" icon .



Note:

You should use the "Save project" function periodically when working on a project because automatic saving of the project does not occur. The only time you are prompted to save the project is when you close the TIA Portal.



 \rightarrow If the configuration is compiled without errors, you see the following:

7.13 Downloading the hardware configuration to the device

 \rightarrow To download your complete CPU to the device, select the \rightarrow "CPU_1512SP [CPU 1512SP F-

Siemena - C. UsersinderDoo mpola_110_CPU15125FF51PM012_110_CPU15125FF51PM PORTAL 🗄 🔛 Save project 🍶 X 10 - X = t (* 1 1 10 10 10 10 2 Gamine 20 新聞課 × 4 Project tree . Devices Tease d. Netu If Device vie Nac 👉 : (ONU 151 2, SP (OPU 151 259 P.1 💽 📰 🖉 🖉 🔛 🗐 🔍 🕏 12 ✓ Catalog 4 fee los Search Add new device 3 8 Datetor Time . Devices & networks
 Oruns12_SP[CPUINS12SPF-1 PN] -No. set 2005P CPU . Contra configuration + maturAdapter of Onlin Program blacks 200 -A 10.00 1 • 🕍 External source firs 8 A PLC tegs
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 CPU1512_3P ***** Cocurrentation settings
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 Cocurrentation settings * CPU1512,5F Online access CPU1912__ CPU1912_SP does ratio 54 Card ReaderfUSB me 00 · Program Słocka Neir (DET) Work may successfully compiled. 685T 193-6P40 Compling trished (errors: C van ii ti L CPU1512.5P . Portal view

1 PN]" folder again and click the \square "Download to device" icon \rightarrow .

 \rightarrow The manager for configuration of connection properties (Extended download) opens.

	Configured access	nodes of "CPU1512_SP"				
and the second se	Device	Device type	Slet	Type	Address	Subnet
	CPU1512_SP	OPU 15125P P-1 PK	1.81	Phone	192,168.0.1	PHAE_1
		Type of the PGIPC inter PGIPC inte	rtuce	Messe select		
		artification to be the attack	third			
			narp 1			-90
	Select target devic	R:	Interfe		Show deepers with	The cartor Address
						T.
Past 122						
Past 122] 					Touro

- \rightarrow First, the interface must be correctly selected. This is done in three steps:
- \rightarrow Type of PG/PC interface: \rightarrow PN/IE

Configured access	nodes of "CPU1512_SP"				
Device	Device type	Slot	Туре	Address	Subnet
CPU1512_SP	CPU 15125P F-1 PN	1 X1	PNINE	192.168.0.1	PN/IE_1
	Tune of the PC/PC inte	dare:	Phote.		
	aciec inte	vfare-	Please select.		
	Plainu inte	A THE REAL OF A	Contraction in the second s		

 \rightarrow PG/PC interface: \rightarrow here e.g.: Intel(R) Ethernet Connection I217-LM

Extended download	to device					_	_	×
	Configured access	nodes of "CPU1512_SP"						
	Device	Device type	Slot	Type.	Address	Subr	iet	
	CPU1512_SP	CPU 15125P F-1 FN	1 X1	PTINE	192.168.0.1	PNA	E.1	
		Type of the PGIPC inte	rface:	PN/IE			•	
		PG/PC inte	rface:	intel(R) Et	hemet Connection (4) 121	9-LM	-	09
	(Connection to interface/su	bnet:	Please select.	10			1
		100 miles	ana an	intel(R) Et	hemet Connection (4) 121	9-LM		1

Note:

and the second second second second

 The PG/PC interface used here depends on the Ethernet interface adapter installed in the programming device.

Statement of the local division of the local	Device.	Device type	Slot	Type	Address	Subr	et
	CPU1512_SP	CPU 15125P F-1 PN	1 X1	PNIGE	192.168.0.1	Philte	_1
		Type of the PG/PC inte	rface:	PN/IE			•
		PC/PC inte	rface	Intel/R1 Fri	server Connection (4) (21)	9-1 M	-
		maine inte		the interior co	service service and the to	e serer :	
	c	Connection to interfaceis	/bnet:	PNRE_1		2 WHI.	

 \rightarrow Connection to interface/subnet \rightarrow "PN/IE_1"

→ The → "Show all compatible devices" field must then be selected and the search for devices in the network must be started by clicking the → $\boxed{\text{Start search}}$ button.

Device Device type Slot Type Address Subnet CPU1512_SP CPU1512SP F-1 PN 1 X1 PNIE 192.168.0.1 PNIE_1 Type of the PGIPC interface: PGIPC interface: Connection to interface/subnet: Itstgatemery Select target device: Show all compatible devices Device type Interface type Address Target device Device type Interface type Interface type Interface type Interface PNIE Access address PNIE Interface type Interface type Interface type Interface type	1000	Configured access	nodes of "CPU1512_SP"					
CPU1512_5P CPU 15125P F-1 PN 1 X1 PNIE 192.168.0.1 PNIE_1 Type of the PGIPC interface: PGI		Device	Device type	Slot	Type	Address	Subnet	
Type of the PG/PC interface: PG/PC interface: <th></th> <th>CPU1512_SP</th> <th>CPU 15125P F-1 PN</th> <th>1 X1</th> <th>PN/IE</th> <th>192.168.0.1</th> <th>PN/IE_1</th> <th></th>		CPU1512_SP	CPU 15125P F-1 PN	1 X1	PN/IE	192.168.0.1	PN/IE_1	
PG/PC interface: Intel(R) Ethernet Connection (4) I219-LM Connection to interface/subnet: PN/E_1 Itt gatemer PN/E_1 Select target device: Show all compatible devices Device Device type Interface type Address Target device PN/IE_1 PN/IE Access address - - - PN/IE_1 PN/IE Access address - - - PN/IE_10 Interface type Address - - - PN/IE_10 Interface type Address - - - - PN/IE_10 Interface type Interface type Address - - - PN/IE_10 Interface type Interface type Access address -			Type of the PG/PC inter	face:	Phile		-	1
Connection to interface/subnet: PN/E_1			PG/PC inter	face:	Intel(R)	Ethernet Connection (4) 12	19-LM *	0
Select target device: Show all compatible devices Device Device type Interface type Address Target device Phile Access address - <t< td=""><td></td><td>c</td><td>connection to interface/sul</td><td>net:</td><td>PN/IE_1</td><td></td><td></td><td>C</td></t<>		c	connection to interface/sul	net:	PN/IE_1			C
Select target device: Device Device type Interface type Address Target device - - PNVIE Access address -			Titgete	ney.				
Device Device type Interface type Address Target device - - PN/IE Access address - Fleich 16D - - PN/IE Access address -		Select target devic	æ:			Show all compatible	e devices	ľ
Fiesh 150 Fiesh 150 Start search		Device	Device type	Inter	ace type	Address	Target devi	e
Nine status information:	Perhaso	-	-	PNUTE		Access address	-	
startsee	sline status informat	ion:				🗍 Display only error	messag) Star	search searc

→ If your CPU is displayed in the "Compatible devices in target subnet" list, select it and start the download. (→ CPU 1512SP F-1 PN → "Load")

tended download	o device	_	_	_	_	_	-
	Configured access	nodes of "CPU1512_SP"					
	Device	Device type	Slot	Type	Address	Subnet	
	CPU1512_SP	CPU 15125P F-1 PN	1 X1	PNRE	192.168.0.1	PNIE_1	
		Type of the PGIPC interfact		PN/IE		•	917
	Con	nection to interface/cuboe	+ P	INE 1		-	
	C.U.I.	reculuit la miena cela done		uic_1			
		141 gatesia	8 () I			N	
	Select target devic	:#:			Show devices with	the same addres	5
	Device	Device type	Interf	ce type	Address	Target device	
Burning.	CPUcommon	CPU-1500 Simula.	Phile		192.168.0.1	CPUcommon	ř
8	-	-	PHILE		Access address	-	
Flash LED							
Online status informati	on:				Display only erro	Start se	earch
Scen completed. 1	compatible devices o	f 1 accessible devices fou	nd.				1
? Retrieving device i	nformation						
Scen and informat	ion retrieval complete	d.					
							- 1
					1	at Can	ral

 \rightarrow You first receive a preview. Continue with \rightarrow "Load".

4II	0	CPU1512_SP	Message Ready for loading.	Action
	0	Simulated module	The download will be performed to a simulated PLC.	
	0	▼ Software	Download software to device	Consistent download
	8	 Download to d Oversette onli 	Objects that do not exist online.	
	-	P. Overning Office	open and exact on the and are overlander.	
	0	Text libraries	Download all alarm texts and text list texts	Consistent download
1			1	

Note:

In the "Load preview", you should see the victor in each line in which actions will be performed. You can see additional information in the "Message" column.

 \rightarrow Select the \rightarrow "Start all" option before completing the download operation with \rightarrow "Finish".

Status 45	9	Target ▼ CPU1512_SP	Message Downloading to device completed without error.	Action
	:	 Start modules 	Start modules after downloading to device. The module *CPU1512_SP* can be started.	Start all
<			п	>

→ The project view is opened again automatically after a successful download operation. A download report appears in the "General" tab of the Info field. This can be helpful for troubleshooting if the download operation was unsuccessful.



7.14 Downloading the hardware configuration to the PLCSIM simulation (optional)

- → If hardware is not available, the hardware configuration can be loaded **alternatively** to a PLC simulation (PLCSIM).
- → First, start the simulation by selecting the \rightarrow "CPU_1512SP [CPU 1512SP F-1 PN]" folder and clicking the \blacksquare \rightarrow "Start simulation" button.



 \rightarrow Confirm the notice "Starting simulation will disable all other online interfaces" with \rightarrow "OK".



 \rightarrow The "PLCSIM" software is started in a separate window in the compact view.



→ The manager for configuration of connection properties (Extended download) opens shortly thereafter.

	Configured access	nodes of "CPU1512_SP"				
and the second se	Device	Device type	Slot	Type	Address	Subnet
	CPU1512_SP	CPU 15125P F-1 PN	1 X1	Pholie	192.168.0.1	PN/IE_1
		Type of the PGIPC interface PGIPC interface	н (Phile PLCSIM		•
	Conr	nection to interface/subne	E (Nease select.	21	• •
		tst gatewa				
	Device	Device type	terror	Reaction in the second	- And - Control	La constant of the
8		brine ipe	inter	race type	Address	Target device
Pleak LED		unit ijt	triage	race type	Address	Target device
Prohito		unit ipt		race type	Address	Stert search
Plash 150	12	Unit ipt		race type	Display only error	Start search messages
Pleak LED	t	unit ipt		race type	Display only error	Start search messages

- \rightarrow The interface must be correctly selected to start. This is done in three steps:
- $\rightarrow~$ Type of PG/PC interface: \rightarrow PN/IE

xtended download	to device		_	_	_	
	Configured access	nodes of "CPU1512_SP"				
	Device	Device type	Slot	Type	Address	Subnet
	CPU1512_SP	CPU 15125P F-1 PN	1 X1	PN/IE	192.168.0.1	PHUIE_1
		Type of the PG/PC interfac	er 📲	PN/IE		•
		PG/PC interfac	e: Pi	ease select		0 10
		an adam ta Tata da a ta ta .	127 - E	PN/IE		12

 \rightarrow PG/PC interface: \rightarrow PLCSIM

Extended downlo	ad to device						×
	Configured access	nodes of "CPU1512_SP"					
	Device	Device type	Slot	Туре	Address	Subnet	
	CPU1512_SP	CPU 15125P F-1 PN	1 X1	PNAE	192.168.0.1	PN/IE_1	
		Tura of the PC PC interfec		Phylip		-	
		type of the Purc Internat		FTUIE			
		Paint internat		PLCSIM		• • •	LS.
	Con	nection to interface/subne	et: Pa	FPLCSIM		0	

 $\rightarrow~$ Connection to interface/subnet \rightarrow "PN/IE_1"

	Configured access	nodes of "CPU1512_SP"					
	Device	Device type	Slat	Type	Address	Subn	et
—	CPU1512_SP	CPU 15125P F-1 PN	1 X1	Phile	192.168.0.1	Phile	_1
		Type of the PG/PC interfac	e: 🖳	Phote			
		PG/PC interfac	e: 🔛	PLCSIM		1.00	099
	Conr	nection to interface/subne	et: Pl	ease select		+	C
		1st gatewa	Di Di	ease select rect at slot '1	x17		T

→ The → "Show all compatible devices" field must then be selected and the search for devices in the network must be started by clicking the → button.

-	Consquied access	nodes of Cruisi2.5r					
	Device	Device type	Slat	Туре	Address	Subr	Het
	Cruisi2_SP	CPU ISI ZPP-I MI	1.×	s Phote	192,168,0.1	Prett	<u>_</u>
		Type of the PG/PC interfact	E)	Phile		-	
		PG/PC interface	8	PLCSIM			* B
	Con	nection to interface/subne	ŧ.	P101E_1			
		titipateus		I.			1
	-	-	P	N/E	Access address	-	ence.
PINER CEN						-	or - an orb
line status information	ić				Display only e	mor message s	tart search

→ If your panel is displayed in the "Compatible devices in target subnet" list, it must be selected before the download can be started. (\rightarrow "CPU-1500 Simulation" \rightarrow "Load")

100 C	Configured access	nodes of "CPU1512_SP"				
	Device	Device type	slot	Type	Address	Subnet
4	CPU1512_SP	CPU 15125P F-1 PN	1 X1	Phote	192.168.0.1	PNNE_1
		Type of the PGIPC interfact		PN/IE		-
	Con	nection to interface/subne	ei pi	WE 1		
		List dashed				
	Select target devic	Device type	Interfa	ice type	Show devices with Address	Target device
8	CPUcommon 	CPU-1500 Simula. 	PHNE		192.168.0.1 Access address	CPUcommon
Flash LED						
Online status informat	ion:				Display only erro	<u>Start search</u>
Scen completed. Retrieving device	t compatible devices o information	f 1 accessible devices fou	nd.			1
Scan and informa	tion retrieval complete	d.				

 \rightarrow You first receive a preview. Continue with \rightarrow "Load".

tatus	10.00	Target	Message	Action
48	0	▼ CPU1512_SP	Ready for loading.	
	0	Simulated module	The download will be performed to a simulated PLC.	
	0	▼ Software	Download software to device	Consistent download
	0	Download to d	Objects that do not exist online.	
	0	Overwrite onli	Objects that exist online and are overwritten.	
	0	Text libraries	Download all alarm texts and text list texts	Consistent download
T				
-			N	

Note:

 In the "Load preview", you should see the size icon in each line in which actions will be performed. You can see additional information in the "Message" column. → The → "Start all" option is now selected before the download operation can be completed with \rightarrow "Finish".

condition i	- illiss				
3	Itatus	and actions after downloa	ding to device		
Status		Target	Message Downloading to device completed without error	Action	
Ť	4	 Start modules 	Start modules after downloading to device. The module "CPU1512_SP" can be started.	Start all	
<			ш		>
			Finish	Load	incel

→ The project view is opened again automatically after a successful download operation. A download report appears in the "General" tab of the Info field. This can be helpful for troubleshooting if the download operation was unsuccessful.



→ The compact view of the PLCSIM simulation is as follows. However, no simulation project has been created yet. If you click the \rightarrow icon, you can now change to the project view.

PLC Siemens	e -×
CPU1512_SP [CPU 1512SP F-1	PN] 🔛
SIEMENS	
0	RUN
	STOP
	MRES
X1 19	2.168.0.1
<no project=""></no>	

→ The PLCSIM simulation is as follows in the project view. Here, you must first click the \rightarrow icon to \rightarrow "Create" a "New project".

Project Edit Execute Options Tools Window Help		Totally Integrated Automation
Croate a new p	Pojoce Project name: Project] Pade C:UtersIMichael Onalles Occuments/Simulation Version: V14 SP1 Author: Michael Opalles Comment: E	
	Create	
6		-

→ If you double-click the → "Device configuration", you can view the downloaded configuration in the project view. You can also set input signals and monitor output signals later for testing the programs. You can change back to the compact view of the simulation by clicking the → icon in the menu bar.



Note:

- You cannot detect hardware configuration errors because a hardware simulation is involved.

7.15 Archiving the project

 \rightarrow To archive the project, select the \rightarrow "Archive" command in the \rightarrow "Project" menu.



 \rightarrow Confirm the prompt "Save project?" with \rightarrow "Yes".

Archive	project (0104:000006)		×
4	Save project? The last saved project is archived archiving to create a backup cop	f. Do you want to save the project 2 y of the current changes?	before
		Ves	No

→ Select a folder in which you want to archive your project and save the project as file type "TIA Portal project archive". (→ "TIA Portal project archive" → "SCE_EN_012-110 Hardware Configuration S7-1500 CPU 1512SP F-1 PN ..." → "Save").

Archiving settings		
Select settings for the ar	Oniving of Project:	
RA Portal project arch	ive.	
Discard restorable de	Ce .	
Add date and time to	the file name	
File name:	012_110_CPU15125P F-1 PN	
New file name:	012_110_CPU1512SP F-1 PH	
Path	Cit	14

7.16 Checklist – step-by-step instructions

The following checklist helps trainees/students to independently check whether all steps of the step-by-step instruction have been carefully completed and enables them to successfully complete the module on their own.

No	Description	Comple
1	Project created.	
2	Slot 1: CPU 1512SP F-1 PN with correct order number	
3	Slot 1: CPU 1512SP F-1 PN with correct firmware version	
4	Slots 23: Digital input modules with the correct order number	
5	Slots 23: Digital input modules with the correct firmware version	
6	Slots 23: Address ranges of the digital input modules correct	
7	Slots 45: Digital output modules with the correct order number	
8	Slots 45: Digital output modules with the correct firmware version	
9	Slots 45: Address ranges of the digital output modules correct	
10	Slot 6: Server module	
11	Modules have set all the correct potential groups for the BaseUnits	
12	Hardware configuration compiled without error message	
13	Hardware configuration downloaded without error message	
14	Project successfully archived	

8 Exercise

8.1 Task description – Exercise

The hardware configuration is to be expanded to include the trainer package **SIMATIC ET 200SP Analog Modules Expansion**. Insert the following missing modules. Select slots 6 and 7 for the analog input modules and slot 8 for the analog output module. The server module is moved to slot 9. Set the address range starting from 64 for the analog modules. Light BaseUnits will be used here.

- 2X AI 2XU/I 2-/4-WIRE HS (order number: 6ES7134-6HB00-0DA1)

Module	Order number	Slot	Address range
AI 2xU/I 2-/4-wire HS	6ES7134-6HB00-0DA1	6	6467
AI 2xU/I 2-/4-wire HS	6ES7134-6HB00-0DA1	7	6871
AQ 2xU/I HS	6ES7135-6HB00-0DA1	8	6467

- 1X AQ 2XU/I HS (order number: 6ES7135-6HB00-0DA1)

Table 1: Analog modules of the CPU 1512SP F-1 PN

8.2 Planning

Plan the implementation of the task by yourself.

8.3 Checklist – Exercise

The following checklist helps trainees/students to independently check whether all steps of the exercise have been carefully completed and enables them to successfully complete the module on their own.

No.	Description	Completed
1	Slots 67: Analog input modules with the correct order number	
2	Slots 67: Analog input modules with the correct firmware version	
3	Slots 67: Address ranges of the analog input modules correct	
4	Slot 8: Analog output module with the correct order number	
5	Slot 8: Analog output module with the correct firmware version	
6	Slot 8: Address range of the analog output module correct	
7	Slot 9: Server module	
8	Modules have set all the correct potential groups for the BaseUnits	
9	Hardware configuration compiled without error message	
10	Hardware configuration downloaded without error message	
11	Project successfully archived	

9 Additional information

More information for further practice and consolidation is available as orientation, for example: Getting Started, videos, tutorials, apps, manuals, programming guidelines and trial software / firmware, under the following link:

siemens.com/sce/s7-1500

Preview "Additional information"

SIMATIC S7: Getting Started, Videos, Tutorials, Apps, Manuals, Trial-SW/Firmware

- > TIA Portal Videos
- > TIA Portal Tutorial Center
- > Getting Started
- > Programming Guideline
- > Easy Entry in SIMATIC S7-1200
- > Download Trial Software/Firmware
- > Technical Documentation SIMATIC Controller
- Industry Online Support App
- > TIA Portal, SIMATIC S7-1200/1500 Overview
- > TIA Portal Website
- SIMATIC S7-1200 Website
- SIMATIC S7-1500 Website

Further Information

Siemens Automation Cooperates with Education siemens.com/sce

SCE Learn-/Training Documents siemens.com/sce/documents

SCE Trainer Packages siemens.com/sce/tp

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Totally Integrated Automation (TIA) **siemens.com/tia**

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